

**PAT-NO:** JP404217353A  
**DOCUMENT-IDENTIFIER:** JP 04217353 A  
**TITLE:** COOLING APPARATUS FOR MULTI-STAGE SEMICONDUCTOR STACK UNIT  
**PUBN-DATE:** August 7, 1992

**INVENTOR-INFORMATION:**

NAME	COUNTRY
KUWABARA, TADAHIRO	
TAKAYASHIKI, TSUKASA	

**ASSIGNEE-INFORMATION:**

NAME	COUNTRY
FUJI ELECTRIC CO LTD	N/A

**APPL-NO:** JP02411414  
**APPL-DATE:** December 18, 1990

**INT-CL (IPC):** H01L023/467

**ABSTRACT:**

PURPOSE: To enhance the productivity of facilities without stopping the operation of the whole of an equipment without giving any adverse effect on the cooling of other semiconductor units after pulling out some semiconductor unit by stacking up drawable semiconductor units within a wind tunnel and attaching a forced air cooling fan

at the upper portion of said wind tunnel.

CONSTITUTION: Drawable semiconductor units are stacked up in multiple stages within a wind tunnel, a cooling apparatus is formed by mounting a forced air cooling fan 4 at the top portion of the wind tunnel. Each semiconductor unit can be pulled out and the vacant space after the pull-out is closed by a shutter 5.

COPYRIGHT: (C)1992, JPO&Japio